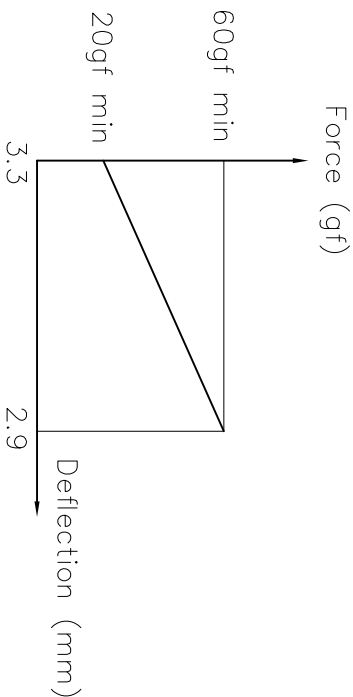


DWG NO.
C-101056

	update remark	8007/11/BO
LTR	REVISION RECORD	APVD DATE



2)Plunger: 0.4um Gold plating over min. 1.4um Nickle plating

DIMENSIONS IN MM TOLERANCES UNLESS OTHER SPECIFIED				MATERIAL				HT TR		FIN	
0	= ±		DWN	APVD	Top-Link Electronics		Top-Link Electronics Ltd.				
0.0	= ±0.15		Benny	Lansing							
0.00	= ±0.10		CHK	REL							
0.000	= ±		Andy								
ANGLE: ±1°				NAME							
SURF TEXTURE: 32/				POGO PIN							
REMOVE BURRS, BREAK SHARP EDGES TO 0.5 MAX				PG-2.0-0.4-1.5-3.5							
DO NOT SCALE PRINT				SCALE		A4		DWG NO.		C-101056	
				1:1						SHEET 1 OF 1	
										REV C	

101056-1
PART NUMBER

USED ON